

# 34 Suppl. ITS

M. Brinson

8/29/02.

CERTIFICATE OF Transmission  
37 C.F.R 1.8

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17 May 2002 *Richard C. Auchterlonie*  
Date Signature

**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****FAX COPY RECEIVED****In re Application of:**

Brent Keeth

Pierre C. Fazan

Serial No.: 08/530,661

Filed: 09/20/1995

For: SEMICONDUCTOR MEMORY  
CIRCUITRY

Group Art Unit: 2814

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Examiner: Wille, Douglas A.

TECHNOLOGY CENTER 2800

Atty. Dkt. No.: 11762.0266.NPUS00

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be

Keeth et al. Serial No.: 08/530,661

construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Respectfully submitted,



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Date: 17 May 2002

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